

ANNUAL CURRICULAR					PLAN (Year)									
NAMR OF THE LECTURER <i>K. Jyothsna</i>					CLASS : <i>ITRECS</i>			Semester : <i>II</i>		Paper : <i>II</i>			<i>Electronic Devices and circuits</i>	
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
<i>Nov</i>	<i>2</i>	<i>2</i>	<i>P-N Junction diode depletion Region</i>	<i>semiconductors Fermi level</i>	-	-	-	-	-	-	-	-	-	
			<i>Barrier potential working in forward</i>	<i>energy band gap</i>	-	-	-	-	-	-	-	-	-	
			<i>and Reverse bias condition, junction</i>	-	<i>will be demonstrated for diode</i>	<i>1</i>	<i>yes</i>	-	-	-	-	-	-	
	<i>3</i>	<i>4H</i>	<i>capacitance, diode current eqn - Effect</i>	-	-	-	-	-	-	-	-	-	-	
			<i>of temperature on Reverse saturation</i>	-	-	-	-	-	-	-	-	-	-	
			<i>current - construction working V-I characteristics</i>	-	-	-	-	-	-	-	-	-	-	
	<i>4</i>	<i>3</i>	<i>and simple application of varactor diode</i>	<i>capacitor</i>	-	-	-	-	-	-	-	-	-	
			<i>zener diode and tunnel diode</i>	-	<i>will be demonstrated Zener diode</i>	<i>1</i>	<i>yes</i>	-	-	-	-	-	-	

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18-19

Electronic Devices  
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Paper : II

ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <i>K. JyBna</i>					CLASS : <i>TMEEG</i>			Semester : <i>II</i>		Paper : <i>II</i>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
Nov	5 <sup>th</sup>	3	Introduction transistor construction, operation and characteristics of CB, CE configurations	-	-	-	-	-	-	-	-	-
			transistor as a switch	Potential divider method	will be shown you tube	1	Yes	-	-	-	-	-
Dec	1	4H	Fixed Bias circuits	-	-	-	-	-	-	-	-	-
			Emitter stabilized Bias circuit, voltage divider Bias stabilization	-	-	-	-	-	-	-	-	-
Dec	2	4H	Introduction, construction, operation and characteristics of FET	-	-	-	-	-	-	-	-	-
			Drain and transfer characteristics, depletion type, and Enhancement-type MOSFET	-	will be shown PPTs	1	Yes	-	-	-	-	-

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Electronic Devices and Circuits

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER K. Jg Bm					CLASS : PHCE			Semester : II		Paper : II			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Dec	3	4+1	VJT construction working, V-I characteristics, VJT as a relaxation oscillator	Resistance potential divider	-	-	-	-	-	-	-	-	-
Dec	4	4+1	structure and working of SCR	-	-	will be shown youtube	1	Yes	-	-	-	-	-
			Two transistors representation	-	-	-	-	-	-	-	-	-	-
			characteristic of SCR application	-	-	-	-	-	-	-	-	-	-
			of SCR for power control	-	-	-	-	-	-	-	-	-	-
Jan	1	4+1	light emitting diode, IR emitters, photo diode	Photo electric effect	-	will be shown model and explained	1	Yes	-	student seminar	1	Yes	-
Jan	2	4	photo transistors, LDR	-	-	-	-	-	-	-	-	-	-

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Electronic Devices  
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ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER K. Jy Bm					CLASS : <u>IME</u>			Semester : <u>II</u>		Paper : <u>II</u>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
Jan	4	4	Rectifiers, Half wave full wave	-	-	-	-	-	student seminar	1	Yes	-
Feb	1	5	Bridge Rectifiers Ripple factor	-	will be shown model and Explain	1	1	Yes	-	-	-	-
			regulation, types of filters	-	-	-	-	-	-	-	-	-
	2	5	L section, $\pi$ section filters	regulators stabilization	-	-	-	-	debate	1	Yes	-
			3 terminal fixed voltage IC regulators	-	-	-	-	-	-	-	-	-
	3	2	principle and working of SMPS	-	will be shown youtube video	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year) 2018-19									
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : II MEC			Semester : IV		Paper : IV			Analog & digital IC Applications	
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
NOV	3rd	4	Def. Basic OP-Amp BCK dia of OP-Amp INV & Non-INV OP-Amp	You Tube Videos	Yes	1hr	Yes	-	-	-	-	-	-	
	4th	5	Virtual ground Adders, subtractors voltage Follower		-	-	-	-	-	-	-	-	-	
DEC	1st	5	OP-Amp Parameters OP-Amp Integrator Differentiator Log amplifier		-	-	-	-	-	-	-	-	-	
	2nd	5	Voltage-current converter. comparators zero crossing detector		-	-	-	-	-	-	-	-	-	
	3rd	4	Instrumentation amp Astable Multivibrator Types of Multivibrators		-	-	-	Chart Preparation	1hr	Yes	-	-	-	
	4th	2	Schmitt trigger sine wave generator square wave generator	working meth od video	Sine wave generator	1hr	Yes	-	-	-	-	-	-	
JAN	1st	5	Triangular wave generator, Active filter low pass, High pass, band pass		-	-	-	-	-	-	-	-	-	
	2nd	3	IC 555 Bk dia, AMV using IC 555 BCD to seven segment		-	-	-	-	-	-	-	-	-	
	3rd	4	BCD to Gray Gray to Binary UP/Down Counter		-	-	-	-	-	-	-	-	-	

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ANNUAL CURRICULAR					PLAN (Year) 2018-19									
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : IIMEC			Semester : IV		Paper : II IC Applications			Analog & digital	
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
Jan	4 <sup>th</sup>	5	USP, SA-ADC Single slope ADC Dual slope ADC	-	-	-	-	-	-	-	-	-	-	
Feb	1 <sup>st</sup>	3	Sigma-Delta ADC R-2R Ladder N/W Binary weighted N/W	-	-	-	-	-	-	-	-	-	-	
	2 <sup>nd</sup>	5	Interfacing of LED Digital clock Shift Reg Parallel to Serial	-	-	-	-	seminar	1hr	Yes	-	-	-	
	3 <sup>rd</sup>	4	serial to Parallel UART, Receiver	Making of UART	-	1hr	Yes	-	-	-	-	-	-	

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ANNUAL CURRICULAR					PLAN (Year) (2018-19) Microcontroller & Interfacing							
NAMR OF THE LECTURER Y. SR IDEVI					CLASS : <u>IMEC</u>			Semester : <u>VI</u>		Paper : (Elective)		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
NOV	I <sup>st</sup>	2	Introduction, Comparison of $\mu P$ & $\mu C$		-	-	-					
	II <sup>nd</sup>	4	Evolution of $\mu C$ s, Development tools for $\mu C$ s		-	-	-		5	-		
	III <sup>rd</sup>	3	Assembler Compiler Simulator		-	-	-	Made the students	1hr	Yes		
	IV <sup>th</sup>	4	Debugging Revision for $\mu C$		-	-	-	Prepare models				
DEC	I <sup>st</sup>	4	Block dia of 8051 PC & memory organization		-	-	-					
	II <sup>nd</sup>	4	Data types & Directives PSW register	Draw the PSW register		1hr	Yes					
	III <sup>rd</sup>	4	Register bank Stack operation Pin diagram of 8051.			-	-	Group discussion	1hr	Yes		
	IV <sup>th</sup>	2	Interrupts Addressing modes of 8051. Data Transfer Instructions.			-	-					

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ANNUAL CURRICULAR					PLAN (Year) (2018-2019) Microcontroller & Interfacing (Elective)								
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : III MEC			Semester : VI		Paper :			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Jan	I <sup>st</sup>	4	Arithmetic Logical Instructions			-	-	-			-	-	-
	II <sup>nd</sup>	2	Addition, subtraction multiplication, division			-	-	-			-	-	-
	III <sup>rd</sup>	3	Arranging a given set of numbers in largest & smallest no's						student seminar	1hr	Yes	-	-
	IV <sup>th</sup>	4	Interfacing of PPI 8255 DAC Interfacing	Working of DAC Interfacing		1hr	Yes	-					
Feb	I <sup>st</sup>	2	Seven segment display LCD display.			-	-	-			-	-	-
	II <sup>nd</sup>	4	Stepper motor Interfacing matrix key pad students seminar			-	-	-			-	-	-
	III <sup>rd</sup>	1	Revision of instructions	Working of LCD display		1hr	Yes	-	student seminar	1hr	Yes	-	-
	IV <sup>th</sup>	4	Revision on Addressing modes.			-	-	-			-	-	-

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ANNUAL CURRICULAR					PLAN (Year) 2018-2019							
NAMR OF THE LECTURER Y-SRIDEVI					CLASS : III MEC			Semester : VI		Paper : VIII cluster A2		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY			CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
Novem	I <sup>st</sup>	1	Introduction for modulation.		-	-	-					
	II <sup>nd</sup>	4	Need for modulation Amplitude modulation Frequency spectrum of AM wave		-	-	-					
	III <sup>rd</sup>	3	Representation of AM Frequency spectrum of FM wave	working FM wave	1hr	Yes	-	student seminar	1hr	Yes	-	
	IV <sup>th</sup>	4	narrow band & wide band FM. Power contents of the carrier & side bands.		-	-	-					
DEC	I <sup>st</sup>	4	FET reactance modulator thermal & shot noise									
	II <sup>nd</sup>	4	Super heterodyne receiver FM receiver, slope detector balanced slope detector	working of super heterodyne receiver	1hr	Yes	-					
	III <sup>rd</sup>	3	Ratio detector communication bands Electromagnetic waves & PPTs & APPS.		-	-	-	student seminar	1hr	Yes	-	
	IV <sup>th</sup>	1	Sampling Thm		-	-	-					

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ANNUAL CURRICULAR					PLAN (Year) 2018-19 Analog & digital Communication (cluster A2)								
NAMR OF THE LECTURER Y.SRIDEVI					CLASS : III MEC				Semester : VI		Paper : VIII		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Jan	I <sup>st</sup>	4	PAM Gen & detection pulse Modulation.	-	-	-	-	-	-	-	-	-	-
	II <sup>nd</sup>	3	PWM Gen & detection	-	-	-	-	Student seminar	1W	Yes	-	-	-
	III <sup>rd</sup>	3	PPM GEN & DETECTION Quantization noise	-	-	-	-	-	-	-	-	-	-
	IV <sup>th</sup>	3	S/N ratio of PCM system relation b/w S/N ratio of a.w.	-	-	-	-	-	-	-	-	-	-
Feb	I <sup>st</sup>	3	Companding Advantages of digital over analog Communication	-	-	-	-	-	-	-	-	-	-
	II <sup>nd</sup>	4	Types of Shift Keying ASK GEN & DET FSK GEN & DET	-	-	-	-	Group discussion	1hr	Yes	-	-	-
			Advantages of Shift Keying over digital Communication.										

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18-19  
Embedded system  
cluster design  
A7

ANNUAL CURRICULAR					PLAN (Year)											
NAMR OF THE LECTURER					CLASS : <u>UIMFes cluster</u>				Semester : <u>VI</u>				Paper : <u>A7</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY							
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date				
NOV	2	3	Introduction to Embedded systems: Embedded systems overview	Block diagram of processor and controllers	-	-	-	-	-	-	-	-	-			
	3	4	Design challenge, processor technology	-	-	-	-	-	-	-	-	-	-			
			IC Technology and design technology	PLD, PAL	-	-	-	-	-	-	-	-	-			
	4	3	Introduction, combinational logic, sequential logic, custom single purpose processor	logic gates	-	-	-	-	-	-	-	-	-			
			Design, RT Level custom single purpose processor	-	-	-	-	-	-	-	-	-	-			
Dec	1	3	Introduction, basic architecture, operation, programmer's view	-	-	-	-	-	-	-	-	-	-			
			ASIPs, and development environment	-	-	-	-	-	-	-	-	-	-			

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER: <u>K. Jyothi</u>					CLASS: <u>TPMES-chips</u> Semester: <u>VII</u>				Paper: <u>Embedded system design</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
			Host and target machines, linker/	Digital computers program	-	-	-	-	-	-	-	-	-
Dec	2	4	Locators for embedded software getting	-	-	-	-	-	-	-	-	-	-
			Embedded software into the target	-	-	-	-	-	-	Student seminar	1	Yes	-
Dec	3	4	system: debugging techniques: Testing on your Host machine	RAM, ROM	-	-	-	-	-	-	-	-	-
Dec	4	3	and instructions set simulators	-	-	will be shown PPT's	1	Yes	-	-	-	-	-
				-	-	-	-	-	-	-	-	-	-
Jan	1	3	Introduction, pulse width modulators	-	-	-	-	-	-	GROUP discussion	1	Yes	-
	2	4	Lcd controllers keypad controllers	-	-	will be shown youtube video	1	Yes	-	-	-	-	-
				-	-	-	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. J. B. M.</i>					CLASS : <i>AI/CS cluster</i>				Semester : <i>VI</i>		Paper : <i>A, Embedded system Design</i>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>Jan</i>	<i>4</i>	<i>3</i>	<i>stepped motor controllers</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
			<i>analog to digital converters and</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>5</i>	<i>2</i>	<i>Real time clocks</i>	<i>Digital clock</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
<i>Feb</i>	<i>1</i>	<i>4</i>	<i>parallel communication serial communication</i>	<i>Asynchronous communication</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
			<i>wireless communication serial protocols; I<sup>2</sup>C</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>2</i>	<i>4</i>	<i>CAN and USB parallel protocols; PCI BUS</i>	<i>RISC processor</i>	<i>will be shown model and Explained</i>	<i>1</i>	<i>Yes</i>	<i>-</i>	<i>Debate</i>	<i>1</i>	<i>Yes</i>	<i>-</i>	
	<i>3</i>	<i>4</i>	<i>and ARM BUS wireless protocols</i>	<i>ARM processor</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
<i>March</i>	<i>1</i>	<i>3</i>	<i>Bluetooth, and IEE 802.11</i>	<i>-</i>	<i>will be shown model and Explained</i>	<i>1</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	

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18-19

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>K. Jyotsna</u>					CLASS : <u>electronics</u>			Semester : <u>VI</u>		Paper : <u>Part A3</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Nov	2	2	Introduction to Power devices	ptype semiconductor	-	-	-	-	-	-	-	-	-
	3	4	Need for semiconductor Power devices, Power diodes	ntype semiconductor	-	1	-	-	-	-	-	-	-
	4	3	Introduction to family of thyristors	-	-	-	-	-	-	-	-	-	-
			structure, I-V characteristics of SCR	-	will be shown model and explained	1	Yes	-	-	-	-	-	-
	4	3	turn-on and turn off characteristics, factors affecting the characteristics of SCR	-	-	-	-	-	-	-	-	-	-
	5	4	control circuits design and protection circuits	-	-	-	-	-	-	-	-	-	-
Dec	1	3	Basic structure working and v-I characteristics of diac, triac	PN Junction diode	will be shown model and explained	1	Yes	-	-	-	-	-	-
	2	4	Basic structure I-V characteristics	-	-	-	-	-	-	-	-	-	-

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Electronics  
A3

ANNUAL CURRICULAR					PLAN (Year)							
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MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
			switching characteristics of IGBT	FET	-	-	-	-	-	-	-	-
Dec	3	4	operation modes, switching characteristics	-	-	-	-	-	-	-	-	-
			power BJT, second break-down, saturation and quasi-saturation state	BJT	will be shown you tube vide	-	-	-	-	-	-	-
			basic chopper circuit, types of choppers (A-D), step-down choppers, step-up	AC, DC circuit & Voltage	-	-	-	-	-	-	-	-
Jan	1	3	chopper, operational d.c chopper ckt using self commutation	-	will be shown PPTs	-	-	-	-	-	-	-
			Hodgen's chopper	-	-	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER K. JB Bra					CLASS : Electronics - dual			Semester : VI		Paper : <u>Electronics</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
JAN	2	4	Need for commutating circuits	Investors	-	-	-	-	-	-	-	-	-
	4	3	and their various types d.c link investors,	-	-	+	-	-	-	-	-	-	-
			parallel capacitor commutated investors	-	-	1	Yes	-	-	-	-	-	-
JAN	5th	3	with and without reactive feed back and its analysis	-	-	-	-	-	-	Debate	1	Yes	-
			series investor, bridge investors	-	-	-	-	-	-	-	-	-	-
FEB	1	5	DC Motor's principle of operation, EMF eqn, Back EMF,	Induced EMF Faraday's law	-	-	-	-	-	student seminar	1	Yes	-
	2	4	Factors controlling Motor speed	Lenz's law	-	-	-	-	-	-	-	-	-
			thyristor based speed control of DC Motor, AC Motor	-	-	1	Yes	-	-	-	-	-	-
			will be shown youtube video	-	-	-	-	-	-	-	-	-	-
			Robot and stator, torque & speed of induction motor	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. JB Bra

Signature of the HOD Y. Sri'devi

Signature of the Principal