

19-20

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019 - 2020

ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <i>K. Jyotsna</i>					CLASS : <i>IHECO</i>		Semester : <i>II</i>		Paper : <i>II - Electronic Devices and Circuits</i>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
<i>Nov</i>	<i>3</i>	<i>5</i>	<i>→ Introduction PN Junction diode</i>	<i>p type semi conductor</i>	-	-	-	-	-	-	-	-
			<i>depletion region potential barriers</i>	<i>Fermi level Energy Band gap</i>	-	-	-	-	-	-	-	-
			<i>→ working of PN junction diode and V-I characteristics</i>	-	<i>will be demonstrated PN junction diode</i>	1	Yes	-	-	-	-	-
	<i>4</i>	<i>5</i>	<i>→ Junction capacitance → Reverse saturation current</i>	-	-	-	-	-	-	-	-	-
			<i>→ construction, working V-I characteristics & application of</i>	-	<i>will be shown youtube video</i>	1	Yes	-	-	-	-	-
			<i>varactor diode, Zener diode, tunnel diode</i>	<i>capacitor</i>	-	-	-	-	-	-	-	-

Signature of the Lecturer *K. Jyotsna*

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NAMR OF THE LECTURER <i>K. Jyotsna</i>					CLASS : <i>IIECS</i>		Semester : <i>II</i>		Paper : <i>II - Electronic Devices and circuits</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>Dec</i>	<i>1</i>	<i>5</i>	<i>UNIT - II</i> → Introduction → Transistor construction	-	-	-	-	-	-	-	-	-	-
			→ operation → CB, CE characteristics	<i>CC configuration</i>	<i>will be demonstrated</i>	<i>1</i>	<i>Yes</i>	-	-	-	-	-	-
	<i>2</i>	<i>5</i>	→ Transistor as a switch → fixed, emitter → potential divider bias	-	-	-	-	-	-	-	-	-	-
			<i>UNIT - 3</i>	-	-	-	-	-	-	-	-	-	-
	<i>3</i>	<i>5</i>	Introduction → construction, operation and characteristics → types of FET/JFE	-	<i>will be shown model and explained</i>	<i>1</i>	<i>Yes</i>	<i>student seminar</i>	<i>1</i>	<i>Yes</i>	-	-	-
			→ MOSFET	-	-	-	-	-	-	-	-	-	-

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NAMR OF THE LECTURER <i>K. Jyothsna</i>					CLASS : <i>ITHECS</i>		Semester : <i>II</i>		Paper : <i>II Electronic Device and circuits</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
			photo diode, photo transistors, LDR	photoelectric Effect	-	-	-	-	-	-	-	-	-
Jan	5th	5	Half wave rectifier full wave rectifier	-	-	-	-	-	-	-	-	-	-
Feb	1	5	bridge rectifier L-section π-section filters	-	-	-	-	-	-	-	-	-	-
			3 pin regulators SMPS, problems	-	will be shown youtube vide	1	Yes		GROUP Discussion	1	Yes	-	-

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER: <u>K. Jyotsna</u>					CLASS: <u>INCEES</u>		Semester: <u>II</u>		Paper: <u>II - Electronic Devices and Circuits</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Dec	4		IMid Exams	-	-	-	-	-	-	-	-	-	-
Jan	1	3	construction, working of V-I characteristics of VJT	potential divider	will be shown you tube video	1	Yes	-	-	-	-	-	-
			→ VJT as a relaxation oscillator	oscillator	-	-	-	-	-	-	-	-	-
Jan	2	5	SCR: structure and working of SCR	-	will be shown model and explained	1	Yes	-	-	-	-	-	-
			Two Transistor Representation of SCR	-	-	-	-	-	-	-	-	-	-
Jan	4	5	light emitting diode, IR emitters	photoelectric effect	-	-	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year) 2019-20									
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : IIMEC		Semester : IV		Paper : IV Analog and Digital IC Applications					
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
NOV	2nd	5	Def Basic Ideal OPAMP Block diagram of OPAMP Inverting & Non Inverting OPAMP	-		-		-						
	3rd	5	Virtual ground Adders, subtractors Voltage Follower OPAMP- Parameters	-		-		-		Student Seminar	1hr	Yes		
	4th	5	OP-AMP- Integrator Differentiator Log Amplifier Voltage to current converter	Log Amplifier		1hr	Yes	-						
DEC	1st	5	Voltage regulator Comparator Zero Crossing detector Instrumentational Amp	-		-		-						
	2nd	5	Active Multipliers Types of Multiplier Schmitt trigger sine wave generator	-		-		-		Student Seminar	1hr	Yes		
	3rd	5	Square wave generator Triangular wave generator Active Filter Low Pass, High Pass, band Pass.	Wagner Filters		1hr	Yes	-						

4th 4 IMed Exams conducted.
 Signature of the Lecturer
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NAMR OF THE LECTURER <i>Y. SRIDEVI</i>					CLASS : <i>I MEC</i>				Semester : <i>IV</i>		Paper : <i>IV Analog & Digital IC Applications</i>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
<i>Jan</i>	<i>1st</i>	<i>4+1</i>	<i>IC 555 block diagram ANV using IC 555 BCD to seven segment BCD to GMEY</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>2nd</i>	<i>2</i>	<i>Gray to Binary UP/down counter</i>	<i>Conversions Gray to Binary & vice versa</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>3rd</i>	<i>3</i>	<i>Universal shift register SA-ADC Single slope ADC</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Student Seminar</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>4th</i>	<i>6</i>	<i>Dual slope ADC Sigma-delta ADC R-2R Ladder network Binary weighted network</i>	<i>A/D converters</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
			<i>Interfacing of LED Digital clock</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Group discussion</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	
<i>Feb</i>	<i>1st</i>	<i>5</i>	<i>Shift registers Parallel to Serial Serial to Parallel UART.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	

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ANNUAL CURRICULAR					PLAN (Year) (2019-20) <i>Microcontroller & Interfacing</i>								
NAMR OF THE LECTURER <i>Y. SRIDEVI</i>					CLASS : <i>III MEC</i>				Semester : <i>VI</i>		Paper : <i>VII (Elective)</i>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>NOV</i>	<i>3rd</i>	<i>4</i>	<i>-</i>	<i>CRT classes conducted</i>	<i>-</i>	<i>1</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
	<i>4th</i>	<i>4</i>	<i>Introduction, Comparison of MP & MC, Evolution of MCS Development tools for MCS, Assembly compiler, Simulator debugger.</i>	<i>-</i>	<i>-</i>	<i>1</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
<i>Dec</i>	<i>1st</i>	<i>4</i>	<i>Block diagram of 8051, PC & memory organization, Data Types & Directives, PSW Register.</i>	<i>-</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
	<i>2nd</i>	<i>4</i>	<i>Register bank, Stack operation, Pin diagram of 8051, Interrupts.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Group discussion</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>
	<i>3rd</i>	<i>4</i>	<i>Addressing modes of Data Transfer Instructions, 8051 Arithmetic, Logical Instructions.</i>	<i>EXPLANATION of Addressing modes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
	<i>4th</i>	<i>3</i>	<i>IMid Exam conducted.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Student Seminar</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>
<i>Jan</i>	<i>1st</i>	<i>3</i>	<i>Addition, Multiplication, Subtraction, division, Arranging a given set of numbers in largest/smallest nos</i>	<i>ALP Examples Addition & Subtraction</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
					<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Student seminar</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>

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NAMR OF THE LECTURER <i>Y. SRIDEVI</i>					CLASS : <i>III MEC</i>				Semester : <i>VI</i>		Paper : <i>VII (Elective)</i>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
<i>Jan</i>	<i>2nd week</i>	<i>4</i>	<i>Interfacing of PPI 8255,</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
		<i>✓</i>	<i>DAC interfacing - seven segment display LCD display</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>3rd</i>	<i>4</i>	<i>stepper motor interfacing matrix keypad students seminar.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Student Seminar</i>	<i>1hr</i>	<i>yes.</i>	<i>-</i>	<i>-</i>	
	<i>4th</i>	<i>3</i>	<i>perplexion 801 instructions of 8051 - Review the Addressing modes.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	

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ANNUAL CURRICULAR					PLAN (Year) 2019-20 Analog & digital communications (cluster A ₂)								
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : III MEC			Semester : VI		Paper : VIII			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
NOV	4 th	5	Need for modulation Amplitude modulation Frequency spectrum of AM wave Representation of AM										
DEC	1 st	5	Frequency spectrum of FM wave narrow band & wideband FM Power contents of the carrier & side bands	—	—	—	—	—	—	Student Seminar	1hr	Yes	—
			FET reactance modulator	—	—	—	—	—	—	—	—	—	—
	2 nd	5	thermal, shot noise super heterodyne receiver FM receiver, slope detector balanced slope & ratio detector	—	—	1hr	Yes	—	Shown you tube video	Quiz on Analog & Digital Communications	1hr	Yes	—
	3 rd	5	communication bands Electromagnetic waves PPS & Applications. Sampling theorem	—	—	—	—	—	—	—	—	—	—
	4 th	4	IMed Exam conducted	—	—	—	—	—	—	—	—	—	—
Jan	1 st	3	PAM Generation & Detection pulse modulation Sampling & Thm	—	—	—	—	—	—	Student Seminar	1hr	Yes	—
	2 nd	4	PWM Generation & Detection PPM Generation & Detection quantization noise S/N Ratio of PCM system	—	—	—	—	—	—	—	—	—	—

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ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <u>Y. SRIDEVI</u>					CLASS : <u>III MEC</u>			Semester : <u>VI</u>		Paper : <u>VIII cluster A2</u>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
Jan	2 nd	4	relation ship b/w S/N ratio & B.W.	—	—	—	—	—	—	—	—	—
	3 rd	4	comparing Advantages of digital over analog communications. types of shift keying.	—	—	—	—	—	—	—	—	—
	4 th	3	Advantages of shift keying over digital communication ASK Generation & detection.	—	—	—	—	—	Student Group discussion	1hr	Yes	—
			FSK Generation & Detection.	—	—	—	—	—	—	—	—	—

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. Jyotsna</i>					CLASS : <i>III MECS cluster</i>		Semester : <i>VI</i>		Paper : <i>A₁ embedded system design</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Nov	4 th	4	Introduction Embedded system	Block diagram Microcontroller	-	-	-	-	-	-	-	-	-
			Design challenge	-	-	-	-	-	-	-	-	-	-
Dec	1	4	processor technology IC technology	-	will be shown ppt's	1	78	-	-	-	-	-	-
	2	3	Design technology	-	-	-	-	-	-	-	-	-	-
	3	4	Introduction combinational logic sequential logic	logic gates AND, OR, NOT	-	-	-	-	-	-	-	-	-
			custom single purpose processor RT level	-	will be shown youtube video	-	-	Yes	-	-	-	-	-

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NAMR OF THE LECTURER <i>K. Jyothna</i>					CLASS : <i>AIITES - cluster</i>		Semester : <i>VI</i>		Paper : <i>A1 Embedded system design</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>Dec</i>	<i>4</i>		<i>— IMid Exams —</i>		<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>
<i>Jan</i>	<i>1</i>	<i>4</i>	<i>→ Introduction → Basic Architecture operation programming</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>student seminar</i>	<i>1</i>	<i>Yes</i>	<i>—</i>
	<i>2</i>	<i>4</i>	<i>View, ASIP → development Environ</i>	<i>—</i>	<i>will be shown PPT's</i>	<i>1</i>	<i>Yes</i>	<i>—</i>	<i>—</i>	<i>student seminar</i>	<i>1</i>	<i>Yes</i>	
			<i>— ment, Host and target machine</i>	<i>computer ip and o/p devices</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>
	<i>4</i>	<i>4</i>	<i>→ linker/locators Getting Embedded software into the target system Debugging techniques</i>	<i>Registers, memory devices</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>
					<i>will be shown Youtube video</i>	<i>1</i>	<i>Yes</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>	<i>—</i>

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NAMR OF THE LECTURER <i>K. Jyotsna</i>					CLASS : <i>TIMESS - cluster</i>		Semester : <i>VI</i>		Paper : <i>A Embedded system design</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
			Testing on your Host machine	-	-	-	-	-	-	-	-	-	-
	<i>5</i>	<i>4</i>	Instruction set simulator	-	-	-	-	-	-	-	-	-	-
			Interdution, pulse width Modulator's LED controller,	-		<i>Will be shown Youtube video</i>	<i>1</i>	<i>Yes</i>	-	-	-	-	-
			key pad, steppes Motor, ADC and Realtime clock	-		-	-	-	-	-	-	-	-
<i>Feb</i>	<i>1</i>	<i>4</i>	Parallel communication, serial, wireless communication	<i>Asynchronous synchronouy communication</i>	-	-	-	-	<i>Quit</i>	<i>1</i>	<i>Yes</i>	-	-
	<i>2</i> <i>3</i>	<i>3</i> <i>3</i>	serial protads IIC, CAN, USB	<i>ARM processor</i>	<i>will be shown model and explained</i>	<i>1</i>	<i>Yes</i>	-	<i>Debate</i>	<i>1</i>	<i>Yes</i>	-	-

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019 - 2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. Jyothi</i>					CLASS : <i>IIITSS - cluster</i>		Semester : <i>VI</i>		Paper : <i>A, Design</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>March</i>	<i>2</i>	<i>3</i>	<i>PCI BUS, ARM BUS, Bluetooth, IFFF, SD, 11</i>	<i>RISC processors</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>

Signature of the Lecturer *K. Jyothi*

Signature of the HOD *Y. Seidani*

Signature of the Principal *[Signature]*

19-20

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019 - 2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. Jyothsna</i>					CLASS : <i>III MEEs-cluster</i>		Semester : <i>VI</i>		Paper : <i>A3 - power electronics</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
NOV	4	4	Power devices ; Introduction, need for semiconductor	p type and n type semiconductors	-	-	-	-	-	-	-	-	-
			Power devices, power diodes, family of thyristor	-	-	-	-	-	-	-	-	-	-
Dec	1	4	I-V characteristics of SCR, turn-on and turn-off factors affecting		will be demonstrated SCR	1	Yes	-	-	-	-	-	-
	2	4	The characteristics of SCR control, protection circuits	Depletion potential Barrier	-	-	-	-	student seminars	1	Yes	-	-
					-	-	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <i>K. Jyothsna</i>					CLASS: <i>WIMES - cluster</i> Semester: <i>VI</i>				Paper: <i>A₃ - Power Electronics</i>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
Dec	3	4	Diac and Triac IGBT	-	Will be demonstrated <i>scf</i>	1	yes	-	Student seminar	1	yes	-
Jan	1	4	Power BJT second breakdown	Transistor	-	-	-	-	-	-	-	-
			saturation and quasi-saturation	-	-	-	-	-	-	-	-	-
Jan	2	4	Power-MOSFET Basic chopper	FET	-	-	-	-	-	-	-	-
			circuit types of choppers, stepdown	step up, step down Transformer	Will be show ppt's	1	yes	-	-	-	-	-
			chopper operation of d.c chopper circuit using									

Signature of the Lecturer *K. Jyothsna*

Signature of the HOD *J. Sridevi*

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CURRICULUM LECTURER WISE 2019 - 2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER K. Jyotsna					CLASS : <u>III M.ECS - cluster</u> Semester : <u>VI</u>				Paper : <u>A3 - power Electronics</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
JUN	4	4	self commutation MOS gate choppers	switch	-	-	-	-	-	-	-	-	-
	5	4	power Inverters; Need for commutating	-	will be shown PPT'S	1	Yes	-	-	-	-	-	-
			ckt and their various types, link Inverters	capacitors	-	-	-	-	-	-	-	-	-
			Parallel capacitor with and without feedback series	-	-	-	-	-	Debate	1	Yes	-	-
			Inverter	Faraday's law Lenz's law	-	-	-	-	-	-	-	-	-
Feb	1	4	DC Motors, principle operation, EMF eqn	-	will be shown you tube video	1	Yes	-	-	-	-	-	-

Signature of the Lecturer K. Jyotsna

Signature of the HOD Y. Sidani

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CURRICULUM LECTURER WISE 2019 - 2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS : <u>JUMBS - cluster</u>				Semester : <u>VI</u>		Paper : <u>A3 - Power Electronics</u>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
			Back emf, factors controlling motor	-	-	-	-	-	-	-	-	-	-
			speed, thyristor based speed control	-	will be shown PPTs	1	Yes	-	-	-	-	-	-
Feb	2	3	AC Motor, rotor and stator, torque	Induced e.m.f	-	-	-	-	Quiz	1	Yes	-	-
			speed of induction motor.	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyoti

Signature of the HOD J. Sridhar

Signature of the Principal [Signature]