

19-20

**SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU**  
**CURRICULUM LECTURER WISE 2019 - 2020**

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>K. JYOTHSNA</u>					CLASS : <u>IIEEE</u>		Semester : <u>I</u>		Paper : <u>I - Basic circuit theory</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	3rd	5	→ Introduction definition of current and voltage sine wave	Atoms electron	-	-	-	-	-	-	-	-	-
	4th	5	Phase relations average value RMS value, AC/DC	Electric field, magnetic field	-	-	-	-	-	-	-	-	-
July	1st	5	V-I relations R, L, C problems	Series and parallel connection	will be shown PPT	-	-	-	-	-	-	-	-
	2nd	4	UNIT-II series & parallel n/w		-	-	-	-	-	-	-	-	-
	3rd	5	R, L, C Kirchoff's laws	Resistor colour code	will be demonstrated R, L, C	1	Yes	-	-	-	-	-	-
	4th	8	Mesh analysis Nodal, star to delta delta to star problems		-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyotsna

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ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER					CLASS :	Semester :	Paper :					
					CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
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	Aug 1st		IMid EXAMS	-	-	-	-	-	-	-	-	-
			Theorems superposition, thevenin		will be demonstrated theorem	4	Yes	-	-	-	-	-
	2nd	3	noting, Reciprocity Maximum, Millman's problems	Current & voltage sources	-	-	-	-	student seminar	1	Yes	-
	3rd	4	UNIT-4 Introduction RC Growth, decay	-	-	-	-	-	-	-	-	-
			RL Growth, decay	Filters	will be shown PPT'S	1	Yes	-	-	-	-	-
	4th	7	RL high, low pass filter RC high, low pass filter	-	-	-	-	-	-	-	-	-

Signature of the Lecturer *K. Jyothi*

Signature of the HOD *J. Sridhar*

Signature of the Principal *[Signature]*

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS :	Semester :	Paper :						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Aug	4th	7	filters differentiating	-	-	-	-	-	-	-	-	-	-
sep	1st	5	integrator problems	multimeter	-	-	-	-	-	-	-	-	-
	2nd		UNIT-5 LCR series II Mid exams	-	will be demonstrated LCR series	1	Yes	-	-	-	-	-	-
	3rd	5	LCR parallel n/w	-	-	-	-	-	Group Discussion	1	Yes	-	-
			quality factor Band width	-	-	-	-	-	-	-	-	-	-
			- series & parallel diff problems	-	-	-	-	-	-	-	-	-	-

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Signature of the HOD *Y. Sridevi*

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ANNUAL CURRICULAR					PLAN (Year) 2019-2020								
NAME OF THE LECTURER Y. SRIDEVI					CLASS : IMEC			Semester : III		Paper : III Digital Electronics			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
JUNE	I <sup>st</sup> week	1	Introduction of number system	—	Shown you tube video	1hr	Yes	—	—	—	—	—	—
	II <sup>nd</sup> week	4	Decimal, Binary, Hexa Decimal, octal, BCD conversions	Number system Importance	—	1hr	Yes	—	—	—	—	—	—
	3 <sup>rd</sup> week	5	1's complement, 2's complement, subtraction Binary Addition & subtraction	9's complement & 10's complement	shown you tube videos	1hr	Yes	—	Student seminar	1hr	Yes	—	—
	4 <sup>th</sup> week	6	Gray code, Excess-3 code, conversion from one to another	—	—	—	—	—	—	—	—	—	—
JULY	I <sup>st</sup> week	5	Laws of Boolean Algebra, Demorgan's Law, Logic gates, universal gates	SOP & POS expressions	shown you tube videos	1hr	Yes	—	—	—	—	—	—
	II <sup>nd</sup> week	4	sum of products, product of sums, simplify the expressions.	—	—	—	—	—	working of universal gates	1hr	Yes	—	—

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**CURRICULUM LECTURER WISE 2019 - 2020**

ANNUAL CURRICULAR					PLAN (Year) 2019-2020								
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : II BSCCME0			Semester : III		Paper : Digital Electronics			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
July	IIIrd week	5	K1 map method Minimization Techniques pairs, quads octets don't care conditions		Shown you solve Videos	1hr	Yes	-	-	-	-	-	-
	4th week	5	Half Adder Full Adder Half Subtractor Full Subtractor		Shown Adders & Subtractors	1hr	Yes	-	QUIZ on Digital Electronics	1hr	Yes	-	-
Aug	1st week	2	Parallel binary Adder Multi plexer De multi plexer	Encoding & Decoding		1hr	Yes	-	-	-	-	-	-
	2nd week	2	Encoder Decoder		Shown various Logic families	1hr	Yes	-	-	-	-	-	-
	3rd week	5	RTL DTL TTL		TTL & CMOS families.	1hr	Yes	-	student seminar	1hr	Yes	-	-
	4th week	6	CMOS RSFF, DFF clocked unclocked & DFF JKFF	Working principle Video		1hr	Yes	-	-	-	-	-	-

Y. Sridevi  
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**CURRICULUM LECTURER WISE 2019 - 2020**

ANNUAL CURRICULAR					PLAN (Year) 2019-2020								
NAMR OF THE LECTURER <i>Y. SRIDEVI</i>					CLASS : <i>IB SOC MEC</i>		Semester : <i>III</i>		Paper : <i>Digital Electronics</i>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>SEP</i>	<i>1st week</i>	<i>4</i>	<i>Toggle FF JKM-SFF Registers (shift left shift right)</i>	—	—	—	—	—	—	—	—	—	—
	<i>2nd week</i>	<i>1</i>	<i>Counters Mod 10 counter</i>	—	—	<i>shown you-tube videos</i>	<i>1hr</i>	<i>Yes</i>	—	<i>Group discussion</i>	<i>1hr</i>	<i>Yes</i>	—
	<i>3rd week</i>	<i>3</i>	<i>Mod 16 counter Down counter PAL, PLA.</i>	—	—	—	—	—	—	—	—	—	—

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**CURRICULUM LECTURER WISE 2019 - 2020**

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. JB Bra</i>					CLASS : <i>III MEE</i> Semester : <i>I</i> Paper : <i>V (B) - Consumed Electronics</i>								
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>June</i>	<i>1</i>	<i>2</i>	<i>Introduction, microwaves</i>	<i>Electromagnetic waves &amp; spectrum</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
	<i>2<sup>nd</sup></i>	<i>4</i>	<i>Microwave oven,</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
			<i>block diagram, LCD</i>	<i>-</i>	<i>will be shown you tube vide</i>	<i>1</i>	<i>yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
			<i>timer with alarm</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
	<i>3<sup>rd</sup></i>	<i>4</i>	<i>single chip antallas</i>	<i>Block diagram of processor</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>
			<i>types of microwave oven</i>	<i>-</i>	<i>will be shown PPT's</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>

Signature of the Lecturer *K. JB Bra*

Signature of the HOD *J. Sridevi*

Signature of the Principal *Sahithi*

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ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <u>K. JYOTHSNA</u>					CLASS : <u>III MEEs</u>			Semester : <u>V</u>		Paper : <u>V(B) - consumer Electronics</u>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
June	4th	4	→ wiring and safety instruction case study	-	-	-	-	-	student seminar	1	Yes	-
July	1st	4	UNIT - 2 → Introduction → electronic controller	zero crossing detector	-	-	-	-	-	-	-	-
			Block diagram of washing machine	-	will be shown you tube video	1	Yes	-	-	-	-	-
	2nd	4	→ hard ware → soft ware types of washing machine	Trial	-	-	-	-	-	-	-	-
	3rd	4	Fuzzy logic washing. Feature of washing machine	-	-	-	-	-	-	-	-	-
	4th	4	UNIT - 3 components of AC → All water A.C system	-	will be shown PPTs	1	Yes	-	-	-	-	-

Signature of the Lecturer K. Jyotsna

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NAME OF THE LECTURER					CLASS :	Semester :	Paper :						
K. Jyothsna					III Mecs	V	(B) - consumer Electronics						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
July	5th		Mid Exams	-	-	-	-	-	-	-	-	-	-
Aug	1st	4	→ All water AC → All water heat → All unit heater	-	-	-	-	-	-	-	-	-	-
	2nd	2	→ central ac, split AC UNIT-4 Facsimile-1, 2	scanners,	will be shown	1	Yes	-	-	-	-	-	-
	3rd	3	→ xerographic copiers → calculators	photo electric effect	-	-	-	-	-	-	-	-	-
	4th	6	→ Internal organization → Digital clock → Block diagram	mod 6, mod 10 counters	-	-	-	-	quiz	1	Yes	-	-
Sep	1st	4	UNIT-5 Digital computers Internet access	-	will be shown PPT's	-	-	-	-	-	-	-	-

Signature of the Lecturer

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>K. Jyotha</i>					CLASS : <i>III BMS</i>	Semester : <i>I</i>	Paper : <i>V(B) - consumer Electronics</i>						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
SEP	1st	4	Function & n/w barcode scanner & decoder	-	will be shown modul and keypad	1	Yes	-	-	-	-	-	-
	2nd		II Mid Exams	-	-	-	-	-	-	-	-	-	-
	3rd	4	EFT ATM set-top boxes	-	-	-	-	-	-	-	-	-	-
OCT	1	4	Digital cable TV, video on demand	Analog and Digital signals	-	-	-	-	debate	1	Yes	-	-

Signature of the Lecturer *K. Jyotha*

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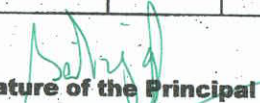
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ANNUAL CURRICULAR					PLAN (Year) 2019-2020								
NAMR OF THE LECTURER Y. SRIDEVI					CLASS : III B. SC (MEC)		Semester : V(A)		Paper : Micro Processors				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	1st week	1	Introduction of Microprocessors	-	-	-	-	-	-	-	-	-	-
	2nd week	3	Block diagram and 8085 control signal & status signal pin out diagram	-	-	-	-	-	-	-	-	-	-
	3rd week	4	Data Transfer Instructions Arithmetic Instructions Logical Instructions Stack I/O Instructions	Instructions	1hr	Yes	-	-	-	-	-	-	-
	4th week	5	Branch group of Instructions Addressing modes Instruction cycle Machine cycle	-	-	-	-	-	-	Student Seminar	1hr	Yes	-
July	1st week	4	opcode Fetch cycle Memory read Memory write I/O read & I/O write	-	-	-	-	-	-	-	-	-	-
	2nd week	3	RAM ROM EPROM	semiconducting memories	-	1hr	Yes	-	-	-	-	-	-

Y. Sridevi  
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NAMR OF THE LECTURER Y. SR IDEVI					CLASS : <u>III MEC</u>		Semester : <u>II (A)</u>		Paper : <u>Micro Processor</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
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JULY	3rd week	4	Programs 8-bit Addition subtraction Multiplications Division.	Examples of Binary Addition Multiplication	-	1hr	Yes	-	-	-	-	-	-
	4th week	4	Largest & Smallest Stack Subroutines Debugging.	Subroutine	-	1hr	Yes	-	-	-	-	-	-
AUG	1st week	4	2Kx8 ROM Diff between I/O mapped & mem mapped	-	-	-	-	-	-	-	-	-	-
	2nd week	1	Revision for Instructions.	-	-	-	-	-	-	-	-	-	-
	3rd week	4	I/O Interfacing Diagram Memory Interfacing Diagram	Interfacing Diagram	-	-	-	-	Quiz on Micro Processor	1hr	Yes	-	-
	4th week	5	Programmable Peripheral device (8255) Modes Pin diagram, I/Os.	-	-	-	-	-	-	-	-	-	-

*Y. Sridevi*  
**Signature of the Lecturer**

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NAMR OF THE LECTURER Y-SRIDEVI					CLASS : <u>II MEC</u>				Semester : <u>II(A)</u> Paper : <u>MICROPROCESSORS.</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
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SEP	1st week	3	8279 Block diagram Display Interface	—	—	—	—	—	—	—	—	—	—
	2nd week	1	Revision of Timing diagrams of opcode Fetch cycle	—	—	—	—	—	Group discussion	1hr	Yes	—	—
	3rd week	4	Simple Temperature Controller stepper motor control in text base	—	—	—	—	—	Student Seminar	1hr	Yes	—	—

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