

II Sem 2020-21

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019-2020

ANNUAL CURRICULAR

PLAN (Year)

Digital electronics

NAMR OF THE LECTURER **K. JYOTHSNA**

CLASS : **IT EEs** Semester : **II**

Paper : **II**

MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	1	5	Number systems and codes: decimal, Binary, Hexa	-	-	-	-	-	-	-	-	-	-
	2	5	decimal, octal conversions	Digital signal	-	-	-	-	-	-	-	-	-
			BCD, Gray and Excess-3 codes	ASCII code	will be shown PPTs	1	Yes	-	-	-	-	-	-
	3	5	complements (1's and 2's)	-	-	-	-	-	-	-	-	-	-
			Addition-subtraction using complement Method	9's complement 10's complement	-	-	-	-	-	-	-	-	-
			Boolean theorems, De Morgan's laws	-	-	-	-	-	-	-	-	-	-
	4	5	Digital IC logic gates	-	-	-	-	-	-	Student seminar	1	NO	out last week
			NAND NOR as universal gates	-	will be show youtube video	1	Yes	-	-	-	-	-	-
			standard representation of logic functions	don't-care condition	-	-	-	-	-	-	-	-	-

Signature of the Lecturer **K. Jyotsna**

Signature of the HOD **J. Sridevi**

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ANNUAL CURRICULAR					PLAN (Year)									
NAMR OF THE LECTURER K. JYOTISNA					CLASS : <u>ITMPel</u>			Semester : <u>II</u>		Paper : <u>II</u>			Digital Electronics	
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
			Minimization techniques k-map	More examples	-	-	-	-	-	-	-	-	-	
Sep	1	5	Half & full adders Half & full subtractors	-	-	-	-	-	-	-	-	-	-	
			Parallel binary adder	-	-	-	-	-	-	-	-	-	-	
	2	4	Multiplexers and demultiplexers	-	will be show you tube video	1	Yes	-	-	-	-	-	-	
			Encoders and decoders	-	-	-	-	-	-	-	-	-	-	
	3	5	TTL logic, CMOS logic operation with truth tables. Differences between CMOS and TTL logic families	IC chip	-	-	-	-	-	GROUP DISCUSSION	1	No	Oct 4th week	
				-	-	-	-	-	-	-	-	-	-	
				RTL, DTL	-	-	-	-	-	-	-	-	-	
	4	5	SR FF clocked RS FF	-	will be show you tube video	1	Yes	-	-	-	-	-	-	

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ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER					CLASS : <u>INRES</u> Semester : <u>II</u> Paper : <u>1</u> <u>Digital Electronics</u>								
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
			DFF, Edge triggering JKFF	-	-	-	-	-	-	-	-	-	-
			conversion of JK FF into DQ TFF	-	1	Yes	-	-	-	-	-	-	-
oct	1	5	serial in serial out and parallel in parallel out	-	-	-	-	-	-	-	-	-	-
			Asynchronous Ripple counter, Mod-6 counter	Mod 6 counter	-	-	-	-	-	-	-	-	-
			synchronous counter - 4 bit parallel binary counter	-	-	-	-	-	-	-	-	-	-
	2	2	General Memory operations	Semiconductor memory	-	-	-	-	-	-	-	-	-
	3	2	ROM, RAM	-	1	Yes	-	quiz	1	Yes	-	-	
	4	5	qualitative PROM EPROM, EEPROM, EAPROM	-	-	-	-	student seminar	1	Yes	-	-	

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2020- 2021

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>Y-SRIDEVI</u>					CLASS : <u>IMEC</u>		Semester : <u>IV</u>		Paper : <u>IV</u> SEM				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	3rd	5	IC555 block dia AMUXing IC555 BCD to 5 even & gm BCD to Gray	-	-	-	-	-	-	Group discussion	1hr	Yes	-
	4th	5	Gray to Binary up/down counter USR	-	-	-	-	-	-	Remedial class	1hr	Yes	-
July	1st	7	SA-ADC single slope ADC dual slope ADC sigma-delta ADC	-	1hr	Yes	-	-	-	student seminar	1hr	Yes	-
	2nd	5	R-2R ladder w/w Binary weighted resistor w/w Interbalancing of	-	-	-	-	-	-	-	-	-	-
	3rd	5	CE D Discrete clock shift registers	-	-	-	-	-	-	quiz	1hr	Yes	-
	4th	5	Parallel to serial Serial to Parallel UART.	-	1hr	Yes	-	-	-	Remedial class	1hr	Yes	-

Signature of the Lecturer

Y. Sridevi

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Y. Sridevi

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CURRICULUM LECTURER WISE 2020- 2021

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS : <u>II MEC</u>				Semester : <u>IV</u>		Paper : <u>IV</u>		
					CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
May	1st	6	Def. Basic ideal OP-Amp. Block dia of OP-Amp Inverting & non-inverting OP-Amp.	-	-	-	-	-	-	-	-	-	
	2nd	5	Virtual ground, Adders, subtractors, Voltage follower	-	Importance of OP-Amp parameters	1hr	Yes	-	-	-	-	-	
	3rd	5	OP-Amp parameters, Integrator, Differentiator, Log Amplifier, Voltage to current converter	-	-	-	-	-	Student seminar	1hr	Yes	-	
	4th		Voltage regulator, Comparators, zero crossing detector, Instrumentation Amp	-	-	-	-	-	remedial class	1hr	Yes	-	
June	1st	4	Adjustable multivibrator, Types of Multivibrators, Schmitt trigger, Sine wave generator	-	Brief note on multivibrators	1hr	Yes	-	-	-	-	-	
	2nd	5	Square wave generator, Triangular wave, Active Filters, Low Pass, High Pass, Band Pass.	-	-	-	-	-	-	-	-	-	

Analog & digital IC applications

Signature of the Lecturer

Y. Sri Devi

Signature of the HOD

Y. Sri Devi

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2020- 2021

ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER <u>Y-SRIDEVI</u>					CLASS : <u>III M E C</u>		Semester : <u>VI</u>		Paper : <u>Microcontroller interfacing</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
May	1st	5	Introduction Comparison of MP & MC, Evolution of MC8.	-	-	-	-	-	-	-	-	-
	2nd	4	Development tools for MC8, Assembly etc. Block dia of 8051.	-	1hr	Yes	-	Remedial class	1hr	Yes	-	-
	3rd	4	PC memory organization. Data Types & directives	-	-	-	-	Remedial class	1hr	Yes	-	-
	4th	4	PSW register, R & banked & stack. Pin diagram of 8051. Interrupts.	-	-	-	-	Student seminar	1hr	Yes	-	-
June	1st	3	Addressing modes & Data Transfer Instructions of 8051.	-	1hr	Yes	-	Group discussion	1hr	Yes	-	-
	2nd	4	Arithmetic & logical Instructions Addition, subtraction of 8bit & 16bit numbers.	-	-	-	-	-	-	-	-	-

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Y Sri Devi

Signature of the HOD
Y Sri Devi

Signature of the Principal
Sri Devi

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2020 - 2021

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <i>Y SRIDEVI</i>					CLASS : <i>III MEC</i>			Semester : <i>VI</i>		Paper : <i>Elective</i>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
<i>June</i>	<i>3rd</i>	<i>4</i>	<i>Multiplications division of 8 bit numbers. Arranging a given set of</i>	<i>write ACP of MUL & DIV.</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>Remedial class</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	
	<i>4th</i>	<i>6</i>	<i>numbers in largest/smallest nos Interfacing of (PP)</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>Student Seminars</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	
<i>July</i>	<i>1st</i>	<i>5</i>	<i>DAC Interfacing seven segment display. LCD display.</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	
	<i>2nd</i>	<i>4</i>	<i>stepper motor interfacing of matrix key pad method.</i>	<i>working principle of stepper motor</i>	<i>-</i>	<i>1hr</i>	<i>Yes</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	<i>-</i>	

Signature of the Lecturer

Y. Sridevi

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2020-21

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CURRICULUM LECTURER WISE 2020-2021

ANNUAL CURRICULAR

PLAN (Year)

NAMR OF THE LECTURER

CLASS : III NECS

Semester : VI

Paper : Embedded system design

MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
April	4	4	Introduction to Embedded system Embedded system overview	about MICRO controller	-	-	-	-	-	-	-	-	-
May	1	4	Design challenge Processor technology	-	will be shown PPTs	1	Yes	-	-	-	-	-	-
			IC technology and design technology	-	-	-	-	-	-	-	-	-	-
	2	4	Introduction combina tional logic, sequential logic, custom single purpose processor design, RT level custom single purpose processor	AND, OR, NOT gates	-	-	-	-	-	-	-	-	-
	3	4	Introduction Basic Architecture, operation programmer's view	-	will be show youtube video	1	Yes	-	-	Student seminar	1	NO	Aug 2nd week
	4	4	ASIP and development environment	-	-	-	-	-	-	-	-	-	-

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CURRICULUM LECTURER WISE 2019-2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER: <u>K. JYOTHSNA</u>					CLASS: <u>III M Sec</u>			Semester: <u>VI</u>		Paper: <u>Embedded system design cluster A,</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	1	4	Host and target machines, linker/locators for Embedded software	Computer input and output devices	-	-	-	-	-	-	-	-	-
	2	4	Getting Embedded software into the target system debugging techniques: testing on your host machine and	Debugging technique	-	-	-	-	Debate	1	NO	Aug 2nd week	-
3	3	4	Instruction set simulators	-	-	-	-	-	-	-	-	-	-
	4	4	Introduction, pulse with modulators, LCD controllers	-	-	-	-	-	-	-	-	-	-
					will be shown ppt's	1	yes	-	-	-	-	-	-
									quit	1	NO	Aug 2nd week	-

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>K. JYOTHSNA</u>					CLASS : <u>TIMEs</u>			Semester : <u>VI</u>		Paper : <u>Embedded system Design cluster A1</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
July	1	4	Key pad controllers	-	-	-	-	-	-	-	-	-	-
			steppers motor controllers	steppers motor	-	1	-	-	-	-	-	-	-
			Analogy to digital converts and real time clocks	Digital clock	will be show you tube video	1	Yes	-	-	-	-	-	-
	2	4	Parallel communication serial, wireless communication	-	-	-	-	-	-	GROUP DISCUSSION	1	NO	Aug 2nd week
	3	4	I ² C, CAN and USB	-	will be show modal and explained	-	-	-	-	student seminar	1	NO	Aug 2nd week
	4	4	PCI, ARM Bluetooth I	-	-	-	-	-	-	-	-	-	-
			I ² C 802-11	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyotsna

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2020-21 P.E

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019-2020

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER					CLASS : III MFS Semester : VI								
					CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
April	4	4	Introduction to power devices	p type and n type semiconductor	-	-	-	-	Power Electronics	-	-	-	
			Need for semiconductor power devices, power diodes, Introduction to family of Thyristors	-	-	-	-	-	-	-	-	-	
			Structure, I-V characteristics of SCR	-	-	-	-	-	-	-	-	-	-
			Turn-on and turn-off characteristics, factors affecting the characteristics of SCR	-	-	-	-	-	-	-	-	-	-
May	1	4	control circuits design and protection	-	will be show model and explained	1	Yes	-	-	-	-	-	
			Basic structure, working and V-I characteristics of diode	-	-	-	-	-	-	-	-	-	
			Student seminar	-	-	-	-	1	NO	Aug 2nd week			
	2	4			will be show youtube video	1	Yes						

Signature of the Lecturer *K. Jyothsna*

Signature of the HOD *Y. Seidani*

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019-2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS : <u>TIMEES</u> Semester : <u>VI</u>				Paper : <u>Power electronics clustered A3</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
	3	4	basic, Basic structure I-V	-	-	-	-	-	-	-	-	-	-
			characteristic switching character	-	will be show modal and explained	1	yes	-	-	-	-	-	-
	4	4	-stly of IGBT power BJT, second	BJT, FET	-	-	-	-	-	-	-	-	-
			Break down, saturation and	-	-	-	-	-	-	-	-	-	-
June	01	4	quasi-saturation state	-	-	-	-	-	-	-	-	-	-
			Basic chopper (cl) - transformer	-	-	-	-	-	-	-	-	-	-
			types of chopper (A-D), step down	-	-	-	-	-	-	-	-	-	-
			chopper, step-up	-	-	-	-	-	-	student seminars	1	NO	July 1st week
			chopper, operation of dchopper	-	will be show youtube video	1	yes	-	-	-	-	-	-
June	02	4	using self-commutated MOSFET chopper	-	-	-	-	-	-	-	-	-	-

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ANNUAL CURRICULAR					PLAN (Year)									
NAME OF THE LECTURER					CLASS : <u>THREE</u> Semester : <u>V</u> Paper : <u>Power Electronics clusters A2</u>									
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
			need for commutating ckt	-	-	-	-	-	will be show ppt	1	yes	-	-	-
			and their various types d.c link	-	-	-	-	-	-	-	-	-	-	-
	3	4	Inverters, parallel capacitor commutated inverter	Inverters	-	-	-	-	-	-	-	-	-	-
			with and without reactive feedback	series connection	-	-	-	-	-	-	-	-	-	-
			and its analysis both inverting series inverter	-	-	-	-	-	-	-	-	-	-	-
July	1	4	Dc Motor, principle of operation	Faraday's laws and lenz's law	-	-	-	-	Quiz	1	NO	-	-	Any mid week
			emf ean, Back emf	-	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyothsna

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2019-2020

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>K. JYOTHSNA</u>					CLASS : <u>IIIrd Sem</u>			Semester : <u>VI</u>		Paper : <u>Power Electronics cluster A</u>			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
	2	4	Factors controlling motor speed	-	-	-	-	-	27/09/20	-	-	-	-
			Thyristor based speed control of DC motor	-	-	-	-	-	28/09/20	-	-	-	-
			AC Motor rotor	Induced e.m.f	-	-	-	-	29/09/20	-	-	-	-
	3	4	stator, torque and speed of AC Motor	-	-	-	-	-	30/09/20	-	-	-	-
	4	4			will be show youtube video	-	-	-	01/10/20	-	-	-	-
						-	-	-	02/10/20	-	-	-	-
						-	-	-	03/10/20	-	-	-	-
						-	-	-	04/10/20	-	-	-	-
						-	-	-	05/10/20	-	-	-	-
						-	-	-	06/10/20	-	-	-	-
						-	-	-	07/10/20	-	-	-	-
						-	-	-	08/10/20	-	-	-	-
						-	-	-	09/10/20	-	-	-	-
						-	-	-	10/10/20	-	-	-	-
						-	-	-	11/10/20	-	-	-	-
						-	-	-	12/10/20	-	-	-	-
						-	-	-	13/10/20	-	-	-	-
						-	-	-	14/10/20	-	-	-	-
						-	-	-	15/10/20	-	-	-	-
						-	-	-	16/10/20	-	-	-	-
						-	-	-	17/10/20	-	-	-	-
						-	-	-	18/10/20	-	-	-	-
						-	-	-	19/10/20	-	-	-	-
						-	-	-	20/10/20	-	-	-	-
						-	-	-	21/10/20	-	-	-	-
						-	-	-	22/10/20	-	-	-	-
						-	-	-	23/10/20	-	-	-	-
						-	-	-	24/10/20	-	-	-	-
						-	-	-	25/10/20	-	-	-	-
						-	-	-	26/10/20	-	-	-	-
						-	-	-	27/10/20	-	-	-	-
						-	-	-	28/10/20	-	-	-	-
						-	-	-	29/10/20	-	-	-	-
						-	-	-	30/10/20	-	-	-	-
						-	-	-	31/10/20	-	-	-	-

Signature of the Lecturer K. Jyotsna

Signature of the HOD Sridewi

Signature of the Principal [Signature]

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU

CURRICULUM LECTURER WISE 2020- 2021

ANNUAL CURRICULAR					PLAN (Year)									
NAMR OF THE LECTURER					CLASS :				Semester :		Paper :			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
June	3rd	5	PWM Generation & detection. PPM Generation & detection.	—	—	—	—	—	—	Quiz on Analog & digital communications	1hr	Yes	—	
	4th	6	Quantization noise S/N ratio of PCM system.	—	—	—	—	—	—	Group discussion	1hr	Yes	—	
July	1st week	6	relation ship b/w S/N ratio & B.W Comparing Advantages of		shown you tube video	1hr	Yes	—	—	—	—	—	—	
	2nd	5	digital over analog communi- cations types of Shift Keying.	—	—	—	—	—	—	Student Seminar	1hr	Yes	—	
	3rd	4	Advantages of Shift Keying over digital communi- cation ASK Generation & Detection.		—	—	—	—	—	Remedial class	1hr	Yes	—	
	4th	5	FSK Generation & Detection.	—	—	—	—	—	—	Remedial class	1hr	Yes	—	

Signature of the Lecturer

Y. Sridevi

Signature of the HOD

Y. Sridevi

Signature of the Principal

Y. Sridevi

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2020 - 2021

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS :	Semester :	Paper :						
Y. SRIDEVI					III MEC	VI	Analog & digital communications cluster A2.						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
May	1st	6	Need for modulation AM, Frequency Spectrum of AM wave, representation of AM.	-	-	-	-	-	-	-	-	-	-
	2nd	4	Frequency Spectrum of FM wave narrow band & wide band FM	-	Impatance of FM & AM	1hr	Yes	-	-	-	-	-	-
	3rd	5	Power contents of the carrier & sidebands. FETC advance modulation.	-	-	-	-	-	-	Student Seminar	1hr	Yes	-
	4th	5	Thermal shot noise super heterodyne receiver, FM receiver slope detector.	-	-	-	-	-	-	-	-	-	-
June	1st	4	balanced slope detector ratio detector communication bands.	-	-	-	-	-	-	Remedial class	1hr	Yes	-
	2nd	5	Electromagnetic waves ppts & applications. Sampling thm	-	ppts of Electro magnetic waves ppts	1hr	Yes	-	-	-	-	-	-

Signature of the Lecturer

Signature of the HOD

Signature of the Principal