

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021- 2022

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER					CLASS :	Semester :	Paper :						
K JYOTHSNA					IMEES	II	II - Digital electronics						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	3	5	Number system and codes: Decimal, Binary	number system	-	-	-	-	-	-	-	-	-
			Hexadecimal, octal systems	-	will be show PPT's	1	yes	-	-	-	-	-	-
	4	5	conversions	ASCII code	-	-	-	-	-	-	-	-	-
	5	4	BCD, Gray and Excess-3 code	9's and 10's complement Method	-	-	-	-	student seminar	1	yes	-	-
July	1	5	complements (9's) Addition-subtraction using complements	-	-	-	-	-	-	-	-	-	-
	2	5	Boolean theorems Demorgan's laws	-	will be show PPT's	1	yes	-	-	-	-	-	-

Signature of the Lecturer *K. Jyotsna*

Signature of the HOD *Y. Sridevi*

Signature of the Principal *[Signature]*

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CURRICULUM LECTURER WISE 2021-2022

ANNUAL CURRICULAR					PLAN (Year)							
NAME OF THE LECTURER					CLASS :	Semester :			Paper :			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
			Digital IC gates NAND & NOR as universal gates.	-	-	-	-	-	-	-	-	-
	3	5	standard representation of logic functions K maps	K maps 4 variable	-	-	-	-	-	-	-	-
	4	5	Half, full adder Half, full subtractor Parallel Binary adder	-	will be show modal and explained	1	Yes	-	Debate	1	Yes	-
Aug	1	5	Multiplexers & demultiplexers Encoders and decoder	-	-	-	-	-	-	-	-	-
	2	4	TTL logic, CMOS logic difference b/w CMOS & TTL	RTL, DTL	will be show youtube video	1	Yes	-	-	-	-	-

Signature of the Lecturer

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021- 2022

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER					CLASS :	Semester :	Paper :			CO-CURRICULAR ACTIVITY			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Aug	3	3	SRFF, clocked RSFF, DFF	-	-	-	-	-	quiz	-	-	-	
	4	5	JKFF, Master slave JKFF conversion JK to D	-	will be show modal & exploited	1	Yes	-	-	-	-	-	
set 1		5	ET Serial, in serial out parallel in, parallel out	Serial connection	-	-	-	-	-	-	-	-	
			Asynchronous counter Mod 10, synchronous 4 bit parallel binary counter	Mod 6, Mod 4 counter	-	-	-	-	-	-	-	-	
set 2	2	5	RAM, ROM EPROM, EAROM Static ROM, dynamic RAM	-	will be show PPT'S	-	-	-	-	-	-	-	

Signature of the Lecturer *k. Jyothi*

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021- 2022

ANNUAL CURRICULAR					D.B.SC PLAN (Year)								
NAMR OF THE LECTURER <u>Y. SRIDEVI</u>					CLASS : <u>DMEC</u>		Semester : <u>IV</u>		Paper : <u>5 Microcontroller & Interfacing</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
May	1st week	5	Introduction - comparison of micro pro c's 8 bit microcontroller.	-	-	-	-	-	-	-	-	-	-
			Evolution of microcontrollers from 4 bit to 32 bit.		shown you tube video.	1hr	Yes	-	Group discussion	1hr	Yes	-	-
			Development tools for microcontroller - used.	-	-	-	-	-	-	-	-	-	-
Jun	2nd week	05	Block diagram of 8051. Pin diagram of 8051.	-	-	-	-	-	-	-	-	-	-
			Program counter memory organization.	-	shown you tube video	1hr	Yes	-	-	-	-	-	-
			data types & directives										

Signature of the Lecturer

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021 - 2022

ANNUAL CURRICULAR					PLAN (Year)							
NAMR OF THE LECTURER					CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
June	3rd week	5	PSW register Register banks & stack.	-	-	-	-	-	-	-	-	-
			Interrupts & timers.	-	shown youtube video	1hr	Yes		-	-	-	-
	4th week	4	Addressing modes of 8051 Instruction set	-	-	-	-	-	student seminar	1hr	Yes	-
July	1st week	5	Jump, Loop, Call Instructions & their usage	-	-	-	-	-	-	-	-	-
			Timer/counter Programming	-	-	-	-	-	remedial class	1hr	Yes	-

IBSC
 CLASS : IMEC Semester : IV Paper : 5 Microcontroller & Interfacing.

Signature of the Lecturer
 Y. Sridevi

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Signature of the Principal
 S. Srinivas

SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021 - 2022

ANNUAL CURRICULAR					II B.Sc PLAN (Year)								
NAMR OF THE LECTURER					CLASS : II MEC		Semester : IV		Paper : 5 Microcontroller & Interfacing				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
July	2nd week	5	Addition & subtraction	-	-	-	-	-	-	-	-	-	-
			Multiplication & division	-	Explanation of MUL & DIV	1hr	Yes	-	-	-	-	-	-
			Largest & Smallest	-	-	-	-	-	-	-	-	-	-
	3rd week	6	8255 Interfacing. seven segment display.	-	Working principle of 8255	1hr	Yes	-	-	-	-	-	-
			displaying information LCD.	-	-	-	-	-	-	-	-	-	-
			stepper motor Interfacing. (unipolar).	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer

Y. Sridevi

Signature of the HOD

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SIR C.R. REDDY COLLEGE FOR WOMEN, ELURU

CURRICULUM PLAN- LECTURER WISE 2021-22

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER : <u>K. Jyotsna</u>					CLASS : <u>IIIrd EES</u>		Semester : <u>IV</u>		Paper : <u>IV - Micro processor systems</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
May	1	2	Introduction of 8086 Micro processor	8085, 8086 Micro processors of difference	-	-	-	-	-	-	-	-	-
June	3	2	8086 architecture	different Microprocessors	-	-	-	-	-	-	-	-	-
	4	2	Pin description Maximum mode	-	will be show PPTs	1	Yes	-	-	-	-	-	-
	5	2	Minimum mode Instruction format	-	-	-	-	-	student seminar	-	-	-	-
July	1	2	addressing modes	-	-	-	-	-	quiz	1	Yes	-	-
July	2	2	Interrupt priority Management	different Interrupt signals	-	-	-	-	-	-	-	-	-
July	3	2	serial communication	-	will be show youtube video	1	Yes	-	-	-	-	-	-
	4	2	parallel communication	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer

K. Jyotsna

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J. Vidya

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SIR C.R. REDDY COLLEGE FOR WOMEN, ELURU

CURRICULUM PLAN- LECTURER WISE 2021-22

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER : K. Jadhava					CLASS : ITNCS		Semester : IV		Paper : IV - Microprocessor systems				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
Aug	1	2	keyboard and display	Interesting different devices	-	-	-	-	-	-	-	-	-
	2	2	DMA controller (8257)		will be show ppt's	-	-	-	-	-	-	-	-
	3	01	introduction to 16/32 bit processors	Different 32 bit processors	-	-	-	-	-	-	-	-	-
	4	02	8086 Architecture organization	8086 BUS	-	-	-	-	-	-	-	-	-
			8086 Instruction set		-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jadhava

Signature of the HOD J. Sridhar

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU

CURRICULUM LECTURER WISE 2021 - 2022

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>Y. SRIDEVI</u>					CLASS : <u>III MEC</u>		Semester : <u>VI</u>		Paper : <u>Micro controller & interfacing</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
April	3rd week	3hrs	Introduction Comparison of MP & MC,	-	Importance of Microcontroller	1hr	Yes	-	-	-	-	-	-
			Evolution of MCs. Development tools for MCs.	-	-	-	-	-	-	-	-	-	-
April	4th week	6hrs	Block diagram of 8051	-	-	-	-	-	-	-	-	-	-
			Program Counter & memory organization.	-	Explanation of PC & memory organization	1hr	Yes	-	Remedial class	1hr	Yes	-	-
			Data Types & Directives.	-	-	-	-	-	-	-	-	-	-
			PSW register & Stack.	-	-	-	-	-	Student Seminar	1hr	Yes	-	-

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>Y-SRIDEVI</u>					CLASS : <u>TI MEC</u>		Semester : <u>VI</u>		Paper : <u>Microcontroller & interfacing</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
May	1st week	6hrs	Pin diagram of 8051	-	-	-	-	-	-	-	-	-	-
			Inter outputs Addressing modes	-	-	-	-	-	-	-	-	-	-
			Data RD - Instructions of 8051	-	Shown you tube video	1hr	Yes	-	QUIZ on Microcontroller	1hr	Yes	-	-
			Arithmetical Logical Instructions	-	Importance of instructions	1hr	Yes	-	-	-	-	-	-
June	2nd week	4hrs	Addition, subtraction of 8 bit &	-	-	-	-	-	-	-	-	-	-
			16 bit numbers multiplication & Division	-	-	-	-	-	-	-	-	-	-

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[Signature]

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ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>Y. SRIDEVI</u>					CLASS : <u>II MEC</u>		Semester : <u>VI</u>		Paper : <u>Microcontroller & Interfacing</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	3rd week	4hrs	Arranging a given set of numbers in largest & smallest nos in a given array.	-	-	-	-	-	-	-	-	-	-
			Smallest nos in a given array.	-	-	-	-	-	-	Remedial class	1hr	Yes	-
	4th week	6hrs	Interfacing of 8255 (PPI)	working principle of 8255	-	1hr	Yes	-	-	-	-	-	-
			DAC Interfacing seven segment display.	-	-	-	-	-	-	-	-	-	-
			LCD Display interfacing	-	Shown you tube video	1hr	Yes	-	student seminar	1hr	Yes	-	-
			stepper motor & key board interfacing	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021 - 2022.

cluster A2

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER <u>Y. SRIDEVI</u>					CLASS : <u>III MEC</u>		Semester : <u>III</u>		Paper : <u>Analog & digital communications.</u>				
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
April	3rd week	4hrs	Need for modulation AM, wave eqn, Spectrum of AM wave.	-	-	-	-	-	-	-	-	-	-
			FM wave eqn narrow band & wideband FM.	-	Importance of sidebands	1hr	Yes	-	-	-	-	-	-
April	4th week	5hrs	Power contents of the carrier & sidebands. FET reactance modulator.	-	FM modulator	-	-	-	Student seminar	1hr	Yes	-	-
			Thermal & shot noise Super heterodyne receiver, FM receiver.	-	-	-	-	-	Remedial class	1hr	Yes	-	-
May	1st week	5hrs	Single slope & Balanced slope detectors	-	-	-	-	-	-	-	-	-	-
			Ratio detector of communication bands Electro magnetic waves PPTs & Applications.	-	Importance of communication bands	1hr	Yes	-	QUIZ	1hr	Yes	-	-

Signature of the Lecturer

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021 - 2022

ANNUAL CURRICULAR					PLAN (Year)								
NAMR OF THE LECTURER					CLASS :	Semester :	Paper :						
Y. SRIDEVI					ITMEC	II SEM	Analog & digital Communications						
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
June	2nd week	1hr	Sampling thm PPM generation & detection. PPM Generation & detection.	-	-	-	-	-	-	Remedial class	1hr	Yes	-
			Quantization noise, S/N ratio of PCM systems	-	shows you tube video	1hr	Yes	-	-	-	-	-	-
	3rd week	5hrs	Relationshi Pb/W S/N ratio & B.W Companding Advantages of digital	-	-	-	-	-	-	Student seminar	1hr	Yes	-
			over analog Communications types of shift Keying	-	Importance of shift Keying	1hr	Yes	-	-	-	-	-	-
June	4th week	5hrs	Advantages of shift keying over digital communication ASK Generation & detection.	-	-	-	-	-	-	-	-	-	-
			FSK Generation & Detection.	-	-	-	-	-	-	Group discussion	1hr	Yes	-

Signature of the Lecturer
Y. Sridevi

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SIR C.R.REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM LECTURER WISE 2021-2022

ANNUAL CURRICULAR				PLAN (Year)								
NAMR OF THE LECTURER <i>K. Jithan</i>				CLASS : <i>Electronics cluster</i>			Semester : <i>VI</i>			Paper : <i>Embedded system Design cluster A1</i>		
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
<i>April</i>	<i>3</i>	<i>4</i>	<i>Embedded systems overview Design Challenge</i>	<i>Microcontroller etc</i>	-	-	-	-	<i>Microcontroller</i>	-	-	-
	<i>4</i>	<i>4</i>	<i>Processor Technology IC, Design Technology</i>	-	-	-	-	-	<i>Processor Technology</i>	-	-	-
<i>May</i>	<i>1</i>	<i>4</i>	<i>Introduction, combinational logic sequential logic Custom single</i>	<i>Logic gates AND, OR, NOT</i>	-	-	-	-	<i>Logic gates</i>	-	-	-
	<i>2</i>	<i>1</i>	<i>Purpose processor Design RT level custom single purpose boards</i>	-	<i>will be show youtube video</i>	<i>1</i>	<i>Yes</i>	-	<i>Group Discussion</i>	<i>1</i>	<i>Yes</i>	-
<i>June</i>	<i>1</i>	<i>4</i>	<i>Introduction Basic Architecture, operation, programming view, ASIP</i>	<i>Processor block diagram</i>	-	-	-	-	<i>Introduction</i>	-	-	-
	<i>2</i>	<i>4</i>	<i>Host and Target Machine</i>	-	-	-	-	-	<i>student seminar</i>	<i>1</i>	<i>Yes</i>	-
			<i>Linker/Loader for Embedded software</i>	-	-	-	-	-	-	-	-	-

Signature of the Lecturer *K. Jithan*

Signature of the HOD *Y. Sidani*

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CURRICULUM LECTURER WISE 2021-2022

ANNUAL CURRICULAR					PLAN (Year)							
NAME OF THE LECTURER <i>K. JBBra</i>					CLASS: <i>Electronics cluster</i> Semester: <i>VI</i> Paper: <i>Embedded system design cluster A</i>							
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY			
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date
June	3	4	Getting embedded software into target system	-	-	-	-	-	Debate	1	Yes	-
	4	4	Debugging techniques Host machine and Instructions of simulator	Debugging techniques	-	-	-	-	-	-	-	-
July	1	4	Pulse width modulator Lcd, keypad controller, stepper motor, ADC and real time clock	Induced EMS ready's laws	Will be show youtube video	1	Yes	-	Quiz	1	Yes	-
July	2	4	IC, CAN, USB PCI, ARM, I2C Bluetooth	-	will be show modal and explained	1	Yes	-	-	-	-	-
	3	1		-	-	-	-	-	-	-	-	-

Signature of the Lecturer *K. JBBra*

Signature of the HOD *J. Sridhar*

Signature of the Principal *Patil*

SIR C.R. REDDY COLLEGE FOR WOMEN, ELURU
CURRICULUM PLAN- LECTURER WISE 2021-22

cluster A3

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER : <u>K. Jyothna</u>					CLASS : <u>Electronics cluster</u> Semester : <u>VI</u> Paper : <u>power electronics</u>								
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
April	3	4	Introduction to power devices, Need for	p type & n type semiconductors	-	-	-	-	-	-	-	-	-
			semiconductors, power devices, power diodes	PN Junction diode	-	-	-	-	-	-	-	-	-
	4	4	Introduction to family of thyristors structure, I-V	-	will be show model and explained	1	Yes	-	-	-	-	-	-
			characteristic of SCR Turn-on and turn-off characteristics, factors affecting the characteristics of SCR	-	-	-	-	-	student seminar	1	Yes	-	-
May	1	4	control circuits design and protection ckt Basic structure,	-	-	-	-	-	-	-	-	-	-
			working and V-I characteristics of diac, Triac	-	will be show model and explained	1	Yes	-	-	-	-	-	-
			Basic structure, I-V characteristics switching characteristics	FET	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyothna

Signature of the HOD J. Sidani

Signature of the Principal [Signature]

SIR C.R. REDDY COLLEGE FOR WOMEN, ELURU

CURRICULUM PLAN- LECTURER WISE 2021-22

ANNUAL CURRICULAR					PLAN (Year)									
NAME OF THE LECTURER : K. Jyothina					CLASS : cluster-electronics				Semester : VI		Paper : cluster A3 - power electronics			
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO-CURRICULAR ACTIVITY					
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date		
	2	1	Power BJT, second Breakdown Regions - saturation	TRANSISTOR	-	-	-	-	-	-	-	-	-	
JUNE	01	4	Basic chopper ckt types of chopper (A-B) step down	step up, step down TRANSFORMER	-	-	-	-	-	-	-	-	-	
			chopper, step up chopper, operation of chopper ckt	-	will be show you tube video	-	-	-	-	-	-	-	-	
JUNE	2	4	using self commutation Morgan's chopper	-	-	-	-	quiz	1	Yes	-	-		
JUNE	3	4	types of chopper Need for commutating circuit and various types	-	-	-	-	-	-	-	-	-	-	
			Investers	-	-	-	-	debate	1	Yes	-	-		
	4	4	d.c link Inverters Parallel capacitor commutated inverter	-	will be show ppt's	-	-	-	-	-	-	-	-	
			with and without reactive feedback and its analysis	-	-	-	-	-	-	-	-	-	-	

Signature of the Lecturer K. Jyothina

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CURRICULUM PLAN- LECTURER WISE 2021-22

ANNUAL CURRICULAR					PLAN (Year)								
NAME OF THE LECTURER : K. Jyothi					CLASS : Electronics cluster Semester : VI Paper : cluster A3 - power Electronics								
MONTH	WEEK	HOURS AVAILABLE	SYLLABUS/ TOPIC	Additional Input/Value Addition Provided/ Taught	CURRICULAR ACTIVITY				CO- CURRICULAR ACTIVITY				
					Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	Activity Conducted	Hours Allotted	Whether Conducted	If not Alternate Date	
July	1	4	series Inverters Bridge Inverters	-	-	-	-	-	-	-	-	-	-
July	2	4	DC Motor, EMF eqn Back emf	Faraday's law Lenz's law	-	-	-	-	GROUP DISCUSSION	1	Yes	-	-
			Factors controlling Motor Speed using Thyristor and without	Induced EMF	will be show youtube video	1	Yes	-	-	-	-	-	-
July	3	1	Thyristor, AC Motor stator, torque and speed of AC Motor.	-	-	-	-	-	-	-	-	-	-

Signature of the Lecturer K. Jyothi

Signature of the HOD J. Sidani

Signature of the Principal